

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jong-Hoon Oh

Docket No.: 1436.108.101

Patent No.: 7,042,777

Issued: May 9, 2006

Title: MEMORY DEVICE WITH NON-VARIABLE WRITE LATENCY

REQUEST FOR CERTIFICATE OF CORRECTION

Certificate of Correction Branch

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

Applicants hereby request an Official Certificate of Correction with respect to the above-identified Patent.

The error(s) listed on the enclosed Certificate of Correction were made by Dicke, Billig & Czaja, PLLC and the Patent Office is hereby authorized to charge Deposit Account 500471 in the amount of \$100.00 to cover the Certificate of Correction fee as set forth under 37 C.F.R. 1.20(a).

Applicants would appreciate the error(s) to be corrected and an Official Certificate of Correction be sent to Applicants' representatives at the address given below.

Respectfully submitted,

Jong-Hoon Oh

By his attorneys,

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Date: August 15, 2007

SED/mlm/cs

By: /Steven E. Dicke/

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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,042,777 Page 1 of 1
DATED : May 9, 2006
INVENTORS : Jong-Hoon Oh

It is certified that error appears in the above-identified patent and that said Letter Patent is hereby corrected as shown below:

Column 3, line 30, delete "an SR flip-flop" and insert in place thereof --a SR flip-flop--.

Column 4, line 21, delete second occurrence of "and provides".

Column 6, line 19, delete second occurrence of "is a".

Column 10, line 36, Claim 19, delete "in response receiving" and insert in place thereof --in response to receiving--.

Column 10, line 44, Claim 19, after "delay after the" insert the following --active state of the row signal--.

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